DATA SHEET

CR2427S
Video driver hybrid amplifier

Product specification
File under Discrete Semiconductors, SC05

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Philips Semiconductors

PHILIPS

Video driver hybrid amplifier CR2427S

FEATURES

- Typical transition times (10 to 90%) with C_L = 8.5 pF:
  - at 35 V (p-p) swing t_r = 2.2 ns; t_f = 2.0 ns
  - at 40 V (p-p) swing t_r = 2.3 ns; t_f = 2.1 ns
  - at 50 V (p-p) swing t_r = 2.5 ns; t_f = 2.2 ns
- Low power consumption
- Minimum small signal bandwidth 130 MHz
- Very fast slew rate; 15000 V/µs
- Excellent grey-scale linearity
- Unconditional stability
- Gold metallization ensures excellent reliability.

APPLICATIONS

It is designed for application in cathode-ray tube (CRT) drivers in high-resolution colour and monochrome monitors.

DESCRIPTION

Hybrid amplifier module mounted in SOT348 package.

PINNING

PIN	DESCRIPTION
1	input
2	ground
3	supply voltage (V_S)
4	ground
5	output

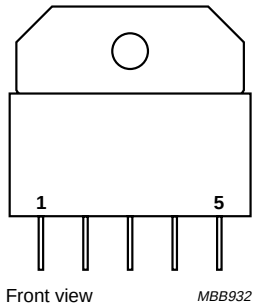


Fig.1 SOT348.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V_S	supply voltage (DC)	–	70	V
T_{mb}	operating mounting base temperature; note 1	–20	+100	°C
T_{stg}	storage temperature	–40	+125	°C

Note

1. To ensure proper thermal contact, a layer of heatsink compound should be applied between module and heatsink.

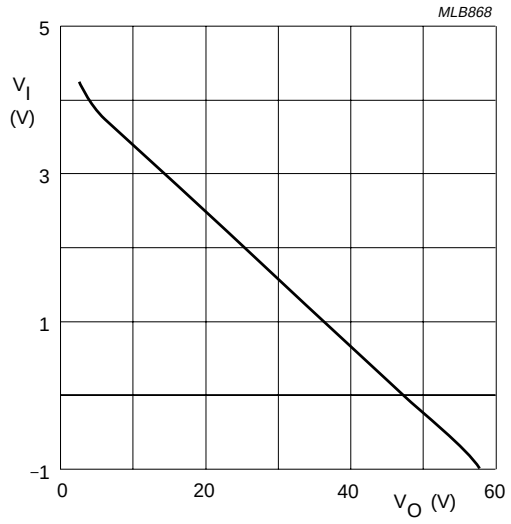
CHARACTERISTICS

T_{mb} = 25 °C; C_L = 8.5 pF; R1 = 215 Ω; C1 = 50 pF (see Fig.10); unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_S = 60 V; 40 V (p-p) output swing with 30 V (DC) offset; unless otherwise specified						
I_S	supply current	input and output open	39	45	51	mA
V_I	input voltage (DC)	input and output open	1.3	1.6	1.9	V
V_O	output voltage (DC)	input and output open	28	31	34	V
t_r	rise time transient response	10 to 90%; note 1	–	2.3	2.9	ns
t_f	fall time transient response	10 to 90%; note 1	–	2.1	2.6	ns
V_S = 65 V; 50 V (p-p) output swing with 32.5 V (DC) offset; unless otherwise specified						
I_S	supply current	input and output open	–	50	57	mA
V_I	input voltage (DC)	input and output open	1.4	1.75	2.1	V
V_O	output voltage (DC)	input and output open	29	32	35	V
t_r	rise time transient response	10 to 90%; note 2	–	2.5	3.2	ns
t_f	fall time transient response	10 to 90%; note 2	–	2.2	3.2	ns
V_S = 60 V or 65 V; 40 V (p-p) or 50 V (p-p) output swing with 30 V (DC) or 32.5 V (DC) offset; unless otherwise specified						
P_{tot}	total power dissipation	50 MHz square wave	–	4.6	6	W
B_s	small signal bandwidth	between –3 dB points; note 3	130	145	–	MHz
V_{tilt}	low frequency tilt voltage	1 kHz square wave	–	1.3	1.5	V
V_{os}	overshoot voltage	varied by C1; see Fig.10	–	3	10	%
NLN	non-linearity	V_O = 5 to 55 V	–	2	5	%
A_V	DC voltage gain	50 Ω source; note 4	11.2	12.4	13.2	
V_G	insertion gain	50 Ω source; note 5	160	180	200	

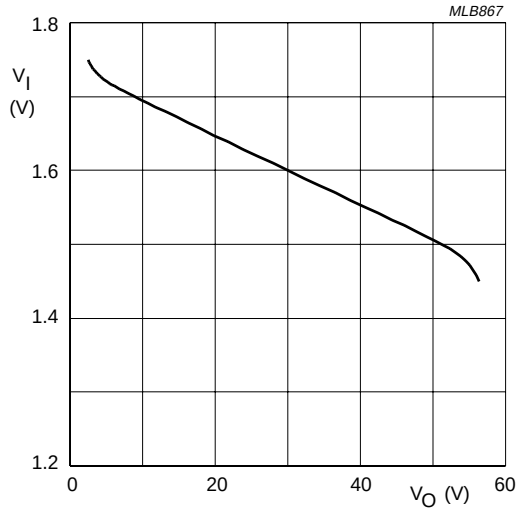
Notes

1. Input signal is a nominal 100 kHz square wave of 3.25 V (p-p), with 1.5 V (DC) offset (50 Ω source).
2. Input signal is a nominal 100 kHz square wave of 3.4 V (p-p), with 1.65 V (DC) offset (50 Ω source).
3. Sine wave output signal: 1 V (p-p).
4. Measured V_O/V_I (Figs 2 and 6) at input test circuit (see Fig.10).
5. Measured V_O/V_I (Figs 3 and 7) at input module (see Fig.10).



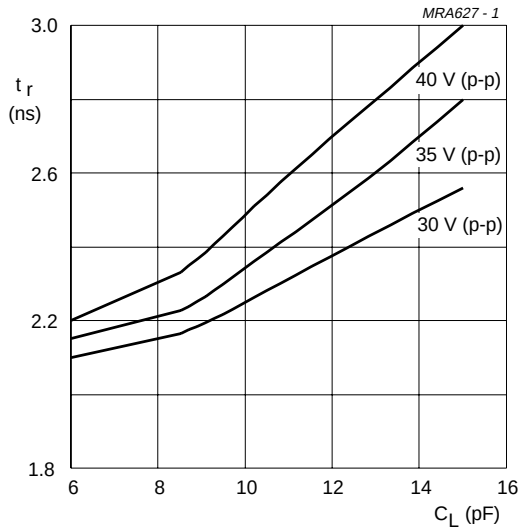
V_S = 60 V; T_{mb} = 25 °C; C_L = 8.5 pF; R1 = 215 Ω; C1 = 50 pF; **40 V (p-p)** output swing with 30 V (DC) offset (see Fig.10).

Fig.2 Input voltage at input test-circuit as a function of output voltage; typical values.



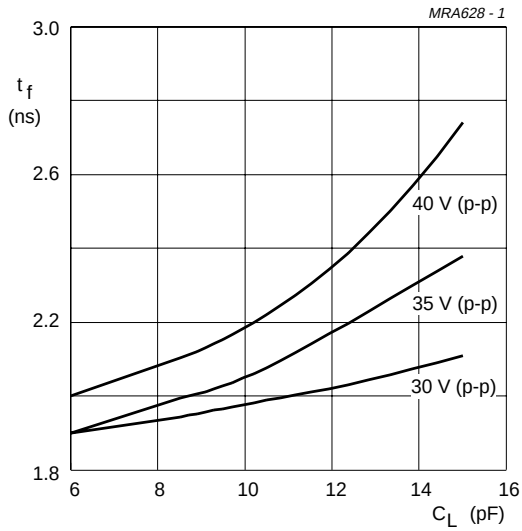
V_S = 60 V; T_{mb} = 25 °C; C_L = 8.5 pF; R1 = 215 Ω; C1 = 50 pF; **40 V (p-p)** output swing with 30 V (DC) offset (see Fig.10).

Fig.3 Input voltage at input module as a function of output voltage; typical values.



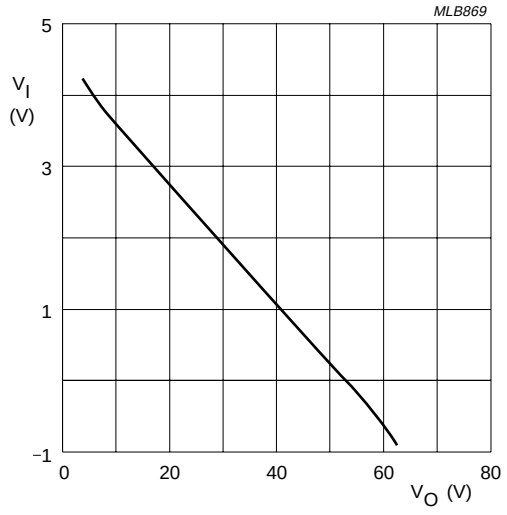
V_S = 60 V; T_{mb} = 25 °C; C_L = 8.5 pF; R1 = 215 Ω; C1 = 50 pF; **40, 35, 30 V (p-p)** output swing with 30 V (DC) offset.

Fig.4 Rise time transient response as a function of load capacitance; typical values.



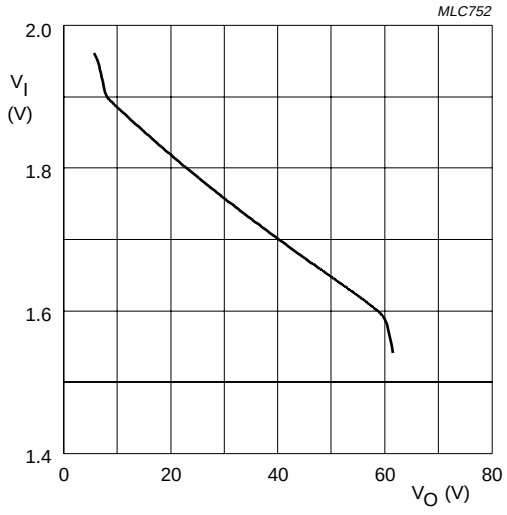
V_S = 60 V; T_{mb} = 25 °C; C_L = 8.5 pF; R1 = 215 Ω; C1 = 50 pF; **40, 35, 30 V (p-p)** output swing with 30 V (DC) offset.

Fig.5 Fall time transient response as a function of load capacitance; typical values.



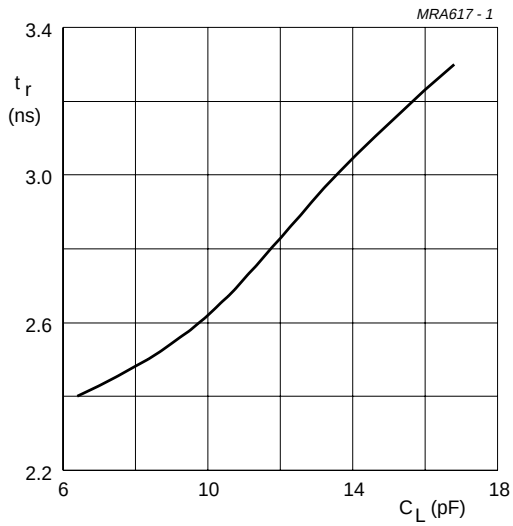
V_S = 65 V; T_{mb} = 25 °C; C_L = 8.5 pF; R1 = 215 Ω; C1 = 50 pF; **50 V (p-p)** output swing with 32.5 V (DC) offset (see Fig.10).

Fig.6 Input voltage at input test-circuit as a function of output voltage; typical values.



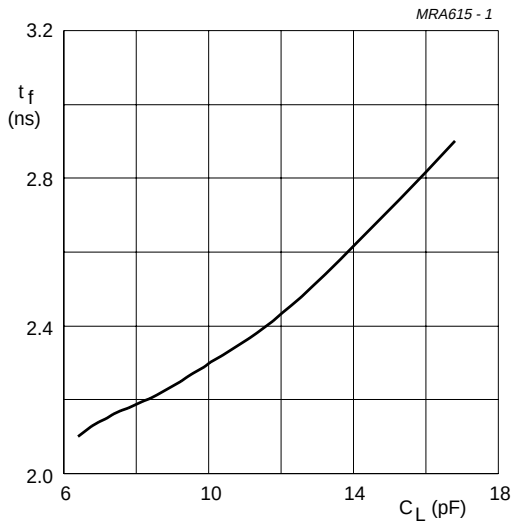
V_S = 65 V; T_{mb} = 25 °C; C_L = 8.5 pF; R1 = 215 Ω; C1 = 50 pF; **50 V (p-p)** output swing with 32.5 V (DC) offset (see Fig.10).

Fig.7 Input voltage at input module as a function of output voltage; typical values.



V_S = 65 V; T_{mb} = 25 °C; C_L = 8.5 pF; R1 = 215 Ω; C1 = 50 pF; **50 V (p-p)** output swing with 32.5 V (DC) offset.

Fig.8 Rise time transient response as a function of load capacitance; typical values.



V_S = 65 V; T_{mb} = 25 °C; C_L = 8.5 pF; R1 = 215 Ω; C1 = 50 pF; **50 V (p-p)** output swing with 32.5 V (DC) offset.

Fig.9 Fall time transient response as a function of load capacitance; typical values.

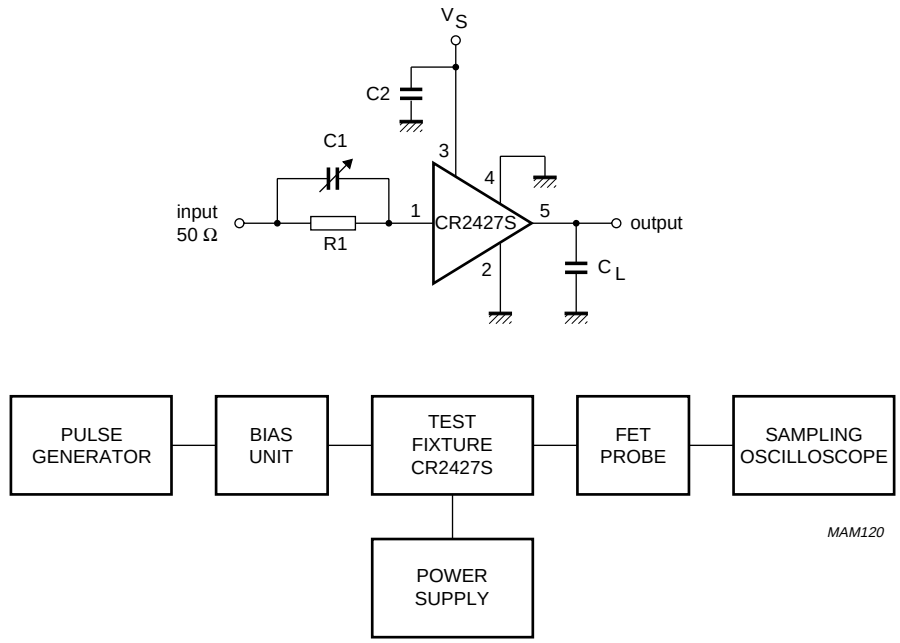


Fig.10 CRT amplifier test-circuit and block diagram.

Components used in test-circuit (see Fig.10)

DESIGNATION	DESCRIPTION	VALUE
C_1	variable capacitor	10 to 120 pF (typ. 50 pF)
C_2	chip capacitor	10 nF
R1	resistor	typ. 215 Ω

Equipment used in test-circuit (see Fig.10)

EQUIPMENT	TYPE DESCRIPTION
Pulse generator	Pico Second; Model 2600B
Bias unit	Pico Second; Model 5555
Power supply	Philips; Model PE1541, 80 V
FET probe	Philips; Model PM8943, attenuation 100 : 1
Sampling oscilloscope	Tektronix; Model 11803, sampling head SD24

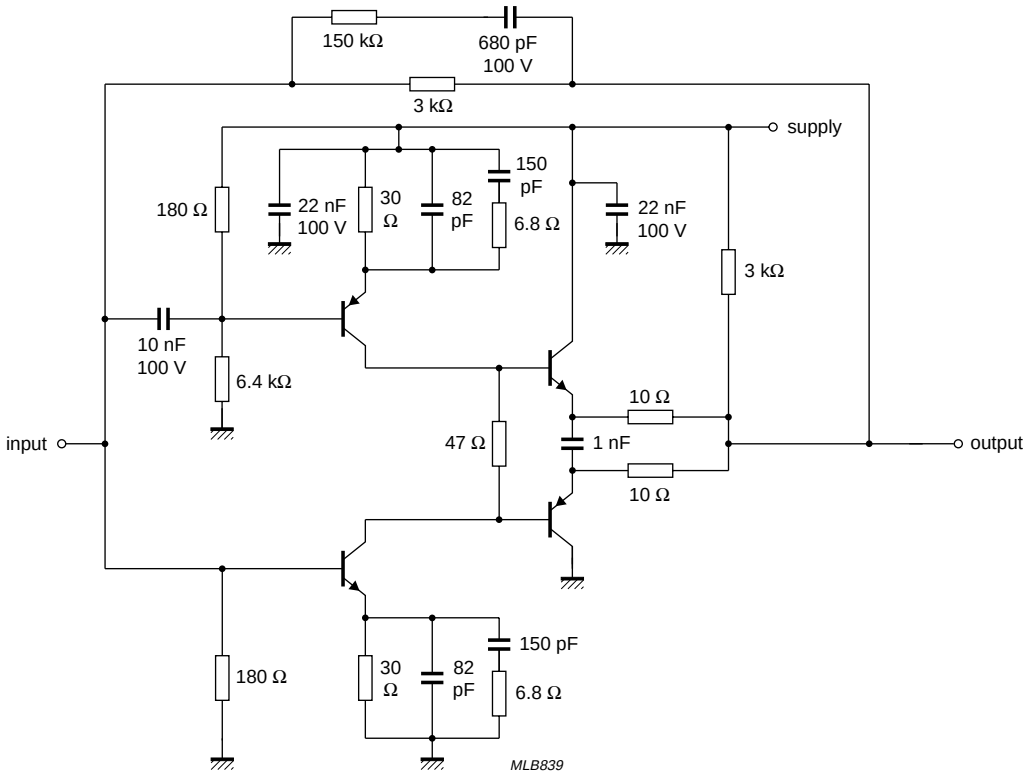
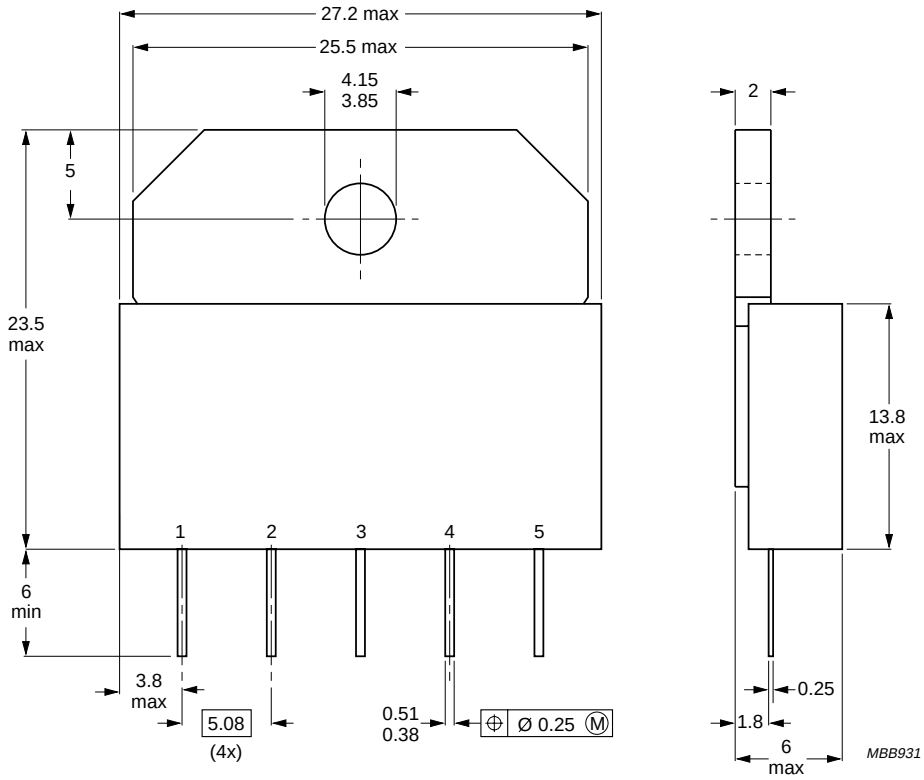


Fig.11 Internal circuit.

PACKAGE OUTLINE



Dimensions in mm.

Heatsink compound must be applied sparingly and evenly distributed.

Fig.12 SOT348.

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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Philips Semiconductors – a worldwide company

Argentina: IEROD, Av. Juramento 1992 - 14.b, (1428)
BUENOS AIRES, Tel. (541)786 7633, Fax. (541)786 9367

Australia: 34 Waterloo Road, NORTH RYDE, NSW 2113,
Tel. (02)805 4455, Fax. (02)805 4466

Austria: Triester Str. 64, A-1101 WIEN, P.O. Box 213,
Tel. (01)60 101-1236, Fax. (01)60 101-1211

Belgium: Postbus 90050, 5600 PB EINDHOVEN, The Netherlands,
Tel. (31)40 783 749, Fax. (31)40 788 399

Brazil: Rua do Rocio 220 - 5th floor, Suite 51,
CEP: 04552-903-SÃO PAULO-SP, Brazil.
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Tel. (011)821-2333, Fax. (011)829-1849

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Tel. (800) 234-7381, Fax. (708) 296-8556

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Tel. (02)773 816, Fax. (02)777 6730

Colombia: IPRELENSO LTDA, Carrera 21 No. 56-17,
77621 BOGOTA, Tel. (571)249 7624/(571)217 4609,
Fax. (571)217 4549

Denmark: Prags Boulevard 80, PB 1919, DK-2300 COPENHAGEN S,
Tel. (032)88 2636, Fax. (031)57 1949

Finland: Sinikalliontie 3, FIN-02630 ESPOO,
Tel. (9)0-50261, Fax. (9)0-520971

France: 4 Rue du Port-aux-Vins, BP317,
92156 SURESNES Cedex,
Tel. (01)4099 6161, Fax. (01)4099 6427

Germany: P.O. Box 10 63 23, 20043 HAMBURG,
Tel. (040)3296-0, Fax. (040)3296 213.

Greece: No. 15, 25th March Street, GR 17778 TAVROS,
Tel. (01)4894 339/4894 911, Fax. (01)4814 240

Hong Kong: PHILIPS HONG KONG Ltd., 15/F Philips Ind. Bldg.,
24-28 Kung Yip St., KWAI CHUNG, N.T.,
Tel. (852)424 5121, Fax. (852)480 6960/480 6009

India: Philips INDIA Ltd, Shivsagar Estate, A Block ,
Dr. Annie Besant Rd. Worli, Bombay 400 018
Tel. (022)4938 541, Fax. (022)4938 722

Indonesia: Philips House, Jalan H.R. Rasuna Said Kav. 3-4,
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Ireland: Newstead, Clonskeagh, DUBLIN 14,
Tel. (01)640 000, Fax. (01)640 200

Italy: PHILIPS SEMICONDUCTORS S.r.l.,
Piazza IV Novembre 3, 20124 MILANO,
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Japan: Philips Bldg 13-37, Kohnan 2-chome, Minato-ku, TOKYO 108,
Tel. (03)3740 5028, Fax. (03)3740 0580

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Tel. 9-5(800)234-7381, Fax. (708)296-8556

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Tel. (03)301 6312, Fax. (03)301 42 43

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Tel. (662)398-0141, Fax. (662)398-3319.

Turkey: Talatpasa Cad. No. 5, 80640 GÜLTEPE/ISTANBUL,
Tel. (0212)279 2770, Fax. (0212)282 6707

United Kingdom: Philips Semiconductors LTD.,
276 Bath Road, Hayes, MIDDLESEX UB3 5BX,
Tel. (0181)730-5000, Fax. (0181)754-8421

United States: 811 East Arques Avenue, SUNNYVALE,
CA 94088-3409, Tel. (800)234-7381, Fax. (708)296-8556

Uruguay: Coronel Mora 433, MONTEVIDEO,
Tel. (02)70-4044, Fax. (02)92 0601

Internet: http://www.semiconductors.philips.com/ps/

For all other countries apply to: Philips Semiconductors,
International Marketing and Sales, Building BE-p,
P.O. Box 218, 5600 MD, EINDHOVEN, The Netherlands,
Telex 35000 phtcnl, Fax. +31-40-724825

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